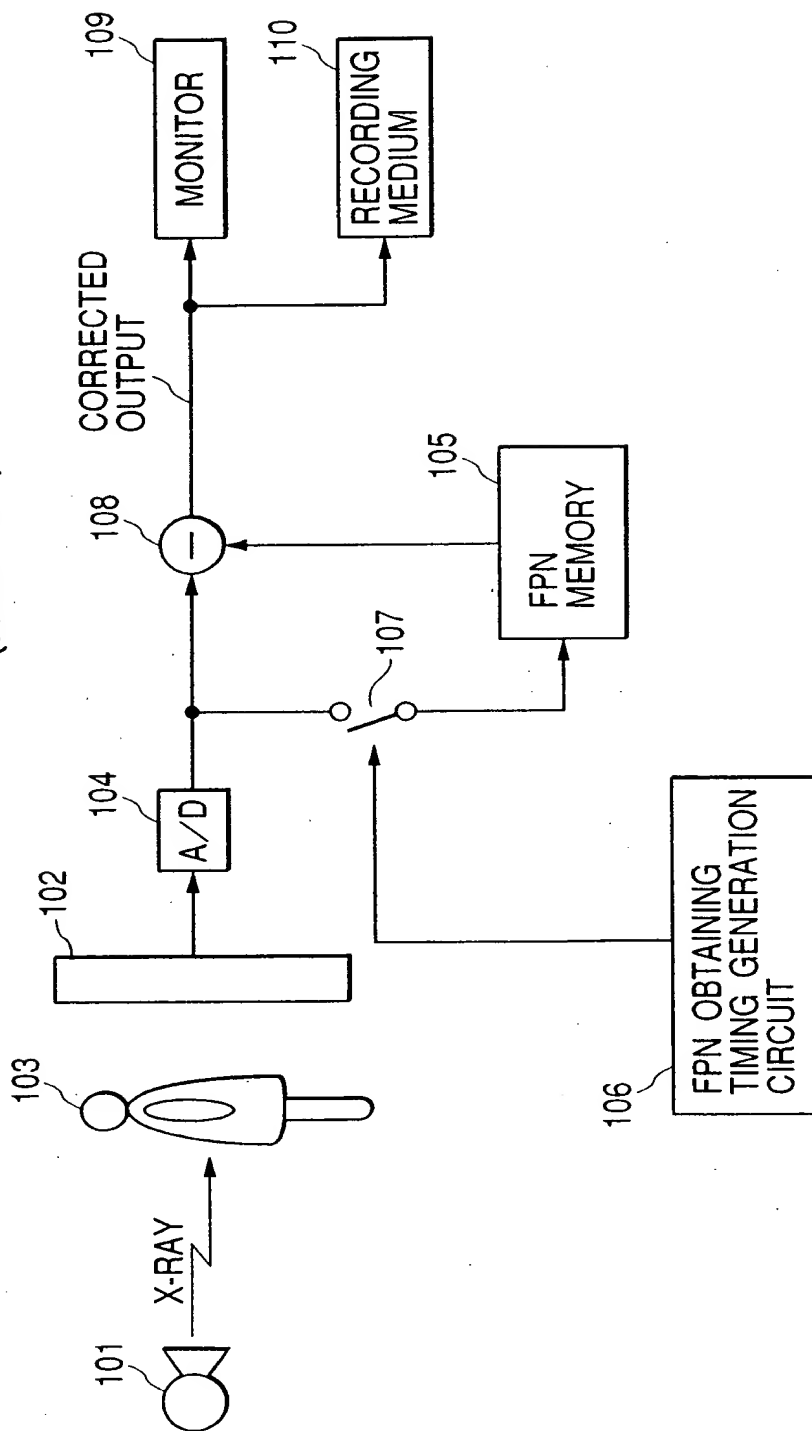


REPLACEMENT SHEET

6/7

FIG. 6

(PRIOR ART)



REPLACEMENT SHEET

7/7



FIG. 7A FPN OBTAINING
TIMING SIGNAL

(PRIOR ART)

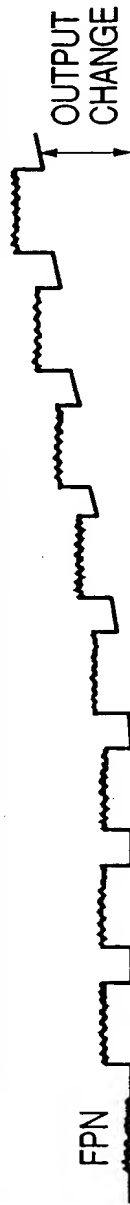


FIG. 7B A/D
OUTPUT

(PRIOR ART)



FIG. 7C CORRECTED
OUTPUT

(PRIOR ART)

